

REMARKS

By way of overview, claims 1-22 have been examined, with claims 1-3 and 5-22 rejected and claim 4 objected to. Claims 11-14 and 21 are canceled, and thus claims 1-10, 15-20, and 22 are now pending.

Claims 6-10, 12, and 21 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite. Also, the specification is objected to because in the paragraph bridging pages 4 and 5, a serial number and filing date are lacking. It is believed that the amendments to the claims and specification overcome this rejection and objection.

Turning to the prior art rejection, claims 1-3, 5-8, and 10-22 are rejected under 35 U.S.C. § 102(b) as being anticipated by Rueth et al. (U.S. Patent No. 5,228,054). While not conceding this validity of the rejection, and merely to advance prosecution, applicant has canceled claims 11-14 and 21. With regard to claims 1-3, 5-8, 10, 15-20, and 22, applicant respectfully traverses this rejection for the reasons set forth below.

Claim 6:

Claim 6 recites, in general, a pseudo-random sequence generator having a linear feedback shift register (LFSR) that generates a first pseudo-random sequence and a sequence of vectors, a first mask circuit that generates a second pseudo-random sequence by selectively combining vector values of each vector of the sequence of vectors, a second mask circuit that generates a third pseudo-random sequence by selectively combining vector values of each vector of the sequence of vectors, and logic circuits that generate a fourth pseudo-random sequence from the second pseudo-random sequence and the third pseudo-random sequence.

Rueth does not disclose, or even suggest, the claimed second mask circuit. As shown in Fig. 1 of the present application, the claimed first mask circuit 130a and second mask circuit 130b each receive the sequence of vectors from the LFSR 120. While Rueth does teach a single mask

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

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Respectfully submitted,

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